

**Amendments to the Specification**

Please replace the paragraph beginning at page 9, line 15, and ending at page 10, line 3, with the following paragraph:

Following the discharge to ground of transistor 420,  $V_3$  is switched to ground, and  $C_{coupling}$  is charged to  $V_{dd}$ . This charge redistributes among the capacitors because node 600 is in a high impedance state. The intermediate voltage at node 600 is not important, provided that the switching of transistors 420 and 410 is not altered. Next,  $V_2$  is switched to ground and

$$Q' = (C_1 + C_2)V_{dd} \quad \text{(Equation 3)}$$

flows into node 600. The difference between the direct current readings represented by Equations 2 and 3, (when  $V_3 = V_{dd}$ , on static mode, and when  $V_3 = 0$  on periodic mode) normalized to  $V_{dd}$  identifies  $C_{coupling}$ .